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Parkinson(10) **Pub. No.: US 2016/0276024 A1**(43) **Pub. Date: Sep. 22, 2016**(54) **METHOD AND APPARATUS FOR DECODING
MEMORY****Publication Classification**(71) Applicant: **Ward Parkinson**, Boise, ID (US)(51) **Int. Cl.**
G11C 13/00 (2006.01)(72) Inventor: **Ward Parkinson**, Boise, ID (US)(52) **U.S. Cl.**
CPC **G11C 13/0023** (2013.01); **G11C 13/0004**
(2013.01)(21) Appl. No.: **14/791,461**(22) Filed: **Jul. 5, 2015****Related U.S. Application Data**(63) Continuation of application No. 13/549,436, filed on
Jul. 14, 2012, now Pat. No. 9,076,521, which is a
continuation of application No. 12/214,144, filed on
Jun. 17, 2008, now Pat. No. 8,223,580.(57) **ABSTRACT**

A thin-film memory may include a thin-film transistor-free address decoder in conjunction with thin-film memory elements to yield an all-thin-film memory. Such a thin-film memory excludes all single-crystal electronic devices and may be formed, for example, on a low-cost substrate, such as fiberglass, glass or ceramic. The memory may be configured for operation with an external memory controller.

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